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Practitioner's Docket No. MI22-1921

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Tran, Luan C.

Application No.: 10/071,453  
Filed: 02/08/02

Group No.: 2813  
Examiner: L.M. Schillinger

Title: A Plurality of Transistors Having Different Active Area Widths and Different Threshold Voltages Defined by STI

Commissioner for Patents  
Washington, D.C. 20231

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/071,453  
Filing Date ..... February 8, 2002  
Inventor ..... Luan C. Tran  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... L.M. Schillinger  
Attorney's Docket No. .... MI22-1921  
Customer No. .... 021567

Title: A Plurality of Transistors Having Different Active Area Widths and Different Threshold Voltages Defined by STI

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References - - See attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 C.F.R. §§ 1.56. Pursuant to Federal Register Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art references are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Date: 2-10-05



D. Brent Kenady  
Reg. No. 40,045

Sheet 1 of 1

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. M122-1921		SERIAL NO. 10/071453	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					APPLICANT Luan C. Tran  FILING DATE February 8, 2002  GROUP 2813			
U.S. PATENT DOCUMENTS								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA							
	AB							
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FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AR		Van Zant, Peter, "Microchip Fabrication - Third Edition," ©1997, page 332.					
	AS							
	AT							
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